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**Council for Trade-Related Aspects
of Intellectual Property Rights**

Original: English

**NOTIFICATION OF LAWS AND REGULATIONS
UNDER ARTICLE 63.2 OF THE TRIPS AGREEMENT**

**SAINT KITTS AND NEVIS: PROTECTION OF LAYOUT-DESIGNS
(TOPOGRAPHIES) OF INTEGRATED CIRCUITS ACT**

Notifying Member	SAINT KITTS AND NEVIS
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Details of the notified legal text

Title	Protection of Layout-Designs (topographies) of Integrated Circuits Act
Subject matter	Layout-designs (topographies) of integrated circuits
Nature of notification	<input checked="" type="checkbox"/> Main dedicated intellectual property law or regulation <input type="checkbox"/> Other law or regulation
Link to legal text	https://members.wto.org/crnattachments/2016/IP/KNA/16_4950_00_e.pdf
Notification status	<input checked="" type="checkbox"/> First notification <input type="checkbox"/> Amendment or revision to notified legal text <input type="checkbox"/> Replacement or consolidation of notified legal text(s)
Previous notification(s) referred to	Not applicable
Brief description of the notified legal text	
An Act to provide for the protection of layout designs (topographies) of integrated circuits and to provide for related or incidental matters	
Language(s) of notified text	English
Date of adoption	
Entry into force	05/11/2007

Notification details

Submission date of notification	03/11/2016
Other information	
Agency or authority responsible	Intellectual Property Office